

Sheet 1 of 1U.S. DEPARTMENT OF COMMERCE
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RCA 88318SERIAL NO.
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614,632****INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97**

(Use several sheets if necessary)

APPLICANT
Wai-Man Lam et al.FILING DATE
July 12, 2000GROUP
2613

U.S. PATENT DOCUMENTS

EXAMINE INITIAL	DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA	5,410,354	04/25/95 K. M. Uz	348	426	
	AB	5,844,636	12/01/98 K. Joseph et al.	348	845	
	AC	5,742,343	04/21/98 B. G. Haskell et al.	348	415	
	AD					
	AE		*EUROPEAN SEARCH REPORT DATED 10 MAY 2004 ATTACHED*			
	AF					
	AG					
	AH					
	AI					

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FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION Yes No
	AL					
	AM					
	AN					
	AO					
	AP					
	AQ					

OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)

	AR	Okubo, S. et al.: "MPEG-2 requirements, profiles and performance verification - Framework for developing a generic video coding standard", SIGNAL PROCESSING. IMAGE COMMUNICATION, ELSEVIER SCIENCE PUBLISHERS, AMSTERDAM, NL, vol. 7, no. 3, 1 Sept. 1995, Pages 201-209, XP004047103.
	AS	Takahashi, H. et al.: "SINGLE-CHIP MPEG2 MP HL DECODER WITH MULTI-DECODE AND SEAMLESS DISPLAY FEATURES", FUJITSU-SCIENTIFIC AND TECHNICAL JOURNAL, FUJITSU LIMITED, KAWASAKI, JP, vol. 36, no. 1, June 2000, pages 48-55, XP001108192.
	AT	Suguri, K. et al., "A SCALABLE ARCHITECTURE OF REAL-TIME MP@HL MPEG-2 VIDEO ENCODER FOR MULTI-RESOLUTION VIDEO", vol. 3653, 27 January 1999, pages 895-904, XP0001189206.

EXAMINER

DATE CONSIDERED

SUBMITTED BY:

REG.NO.:

DATE:

GUY H. ERIKSEN

41,736

June 7, 2004